

PATENT
0630-1162P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant:

GIL, Gyoung-Seon

Conf.: 7079

Appl. No.:

09/689,746

Group: 2811

Filed:

October 13, 2000

Examiner: LOKE, S.

For:

THIN FILM TRANSISTOR AND FABRICATION METHOD
THEREFOR

RECEIVED
JAN 23 2003
TECHNOLOGY CENTER 2800

LARGE ENTITY TRANSMITTAL FORM

Assistant Commissioner for Patents
Washington, DC 20231

January 22, 2003

Sir:

Transmitted herewith is an amendment in the above-identified application.

- The enclosed document is being transmitted via the Certificate of Mailing provisions of 37 C.F.R. § 1.8.
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The fee has been calculated as shown below:

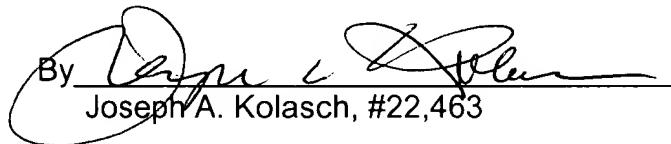
	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR		PRESENT EXTRA	RATE	ADDITIONAL FEE
TOTAL	5	-	20	=	0	\$18	\$0.00
INDEPENDENT	1	-	3	=	0	\$84	\$0.00
<input type="checkbox"/> FIRST PRESENTATION OF A MULTIPLE DEPENDENT CLAIM						\$280	\$0.00
						TOTAL	\$0.00

- Petition for (0) month(s) extension of time pursuant to 37 C.F.R. §§ 1.17 and 1.136(a). \$0.00 for the extension of time.
- No fee is required.
- A check in the amount of \$0.00 is enclosed.
- Please charge Deposit Account No. 02-2448 in the amount of \$0.00. This form is submitted in triplicate.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By 
Joseph A. Kolasch, #22,463

JAK/ALB:bb
0630-1162P

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ATTACHMENT

(Rev. 01/22/01)



PATENT
0630-1162P

#111/AMDT
D
01/24/03
VS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: GIL, Gyoung-Seon

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AMENDMENT

Assistant Commissioner
Washington, D.C. 20231

January 22, 2003

Dear Sir:

Responsive to the Examiner's Office Action dated October 22, 2002, the following amendments and remarks are respectfully submitted in connection with the above-identified application.

IN THE CLAIMS

Please amend the claims as follows:

1. (Thrice amended) A thin film transistor, comprising:

a stepped substrate provided with a sidewall between upper and lower portions thereof;

an active layer formed on the stepped substrate;